

Notice of References Cited	Application/Control 09/939,067	Applicant(s)/Patent Under Reexamination MCDANIEL, DARYL V. <i>pl</i>	
	Examiner Dorna-K. Mason	Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,675,324	01-2004	Marisetty et al.	714/30
	B	US-5,768,599	06-1998	Yokomizo, Takashi	710/260
	C	US-6,003,108	12-1999	Thielen, David	710/260
	D	US-2003/0051190	03-2003	MARISSETTY et al.	714/11
	E	US-6,092,144	07-2000	Thielen, David	710/269
	F	US-2002/0032822	03-2002	Plummer et al.	710/260
	G	US-6,587,966	07-2003	Chaiken et al.	714/34
	H	US-6,606,676	08-2003	Deshpande et al.	710/262
	I	US-6,615,288	09-2003	Herzi, Dirie N.	710/10
	J	US-2002/0116563	08-2002	Lever, Paul D.	710/260
	K	US-6,694,457	02-2004	McKee, Bret A.	714/38
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Jeffay, Kevin, et al., "Accounting for Interrupt Handling Costs in Dynamic Priority Task Systems," Dec. 1-3, 1993, IEEE, Proceedings: Real-Time Systems Symposium, p. 212-221.
	V	Schaelicke, Lambert, et al., "Profiling I/O Interrupts in Modern Architectures," Aug. 29 - Sept. 1, 2000, Proceedings: 8th International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems, p. 115-123.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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